



1/9

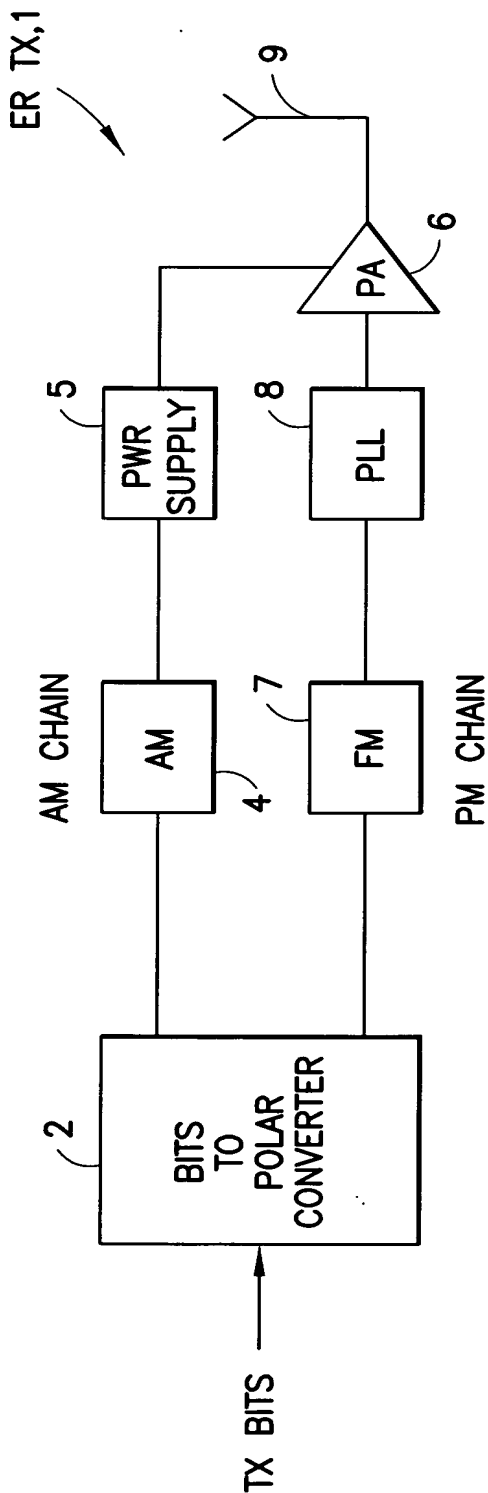


FIG.1
PRIOR ART

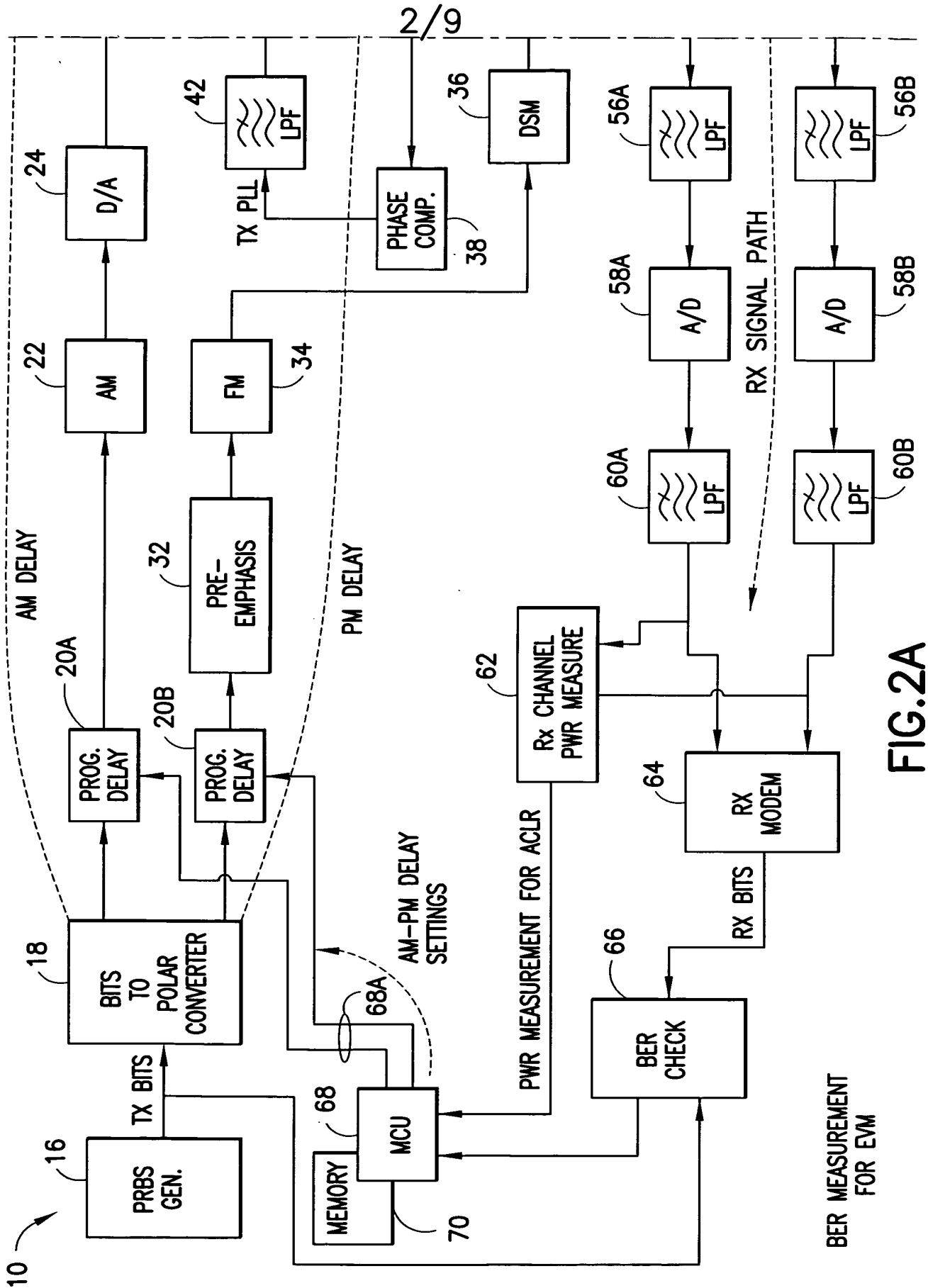


FIG.2A

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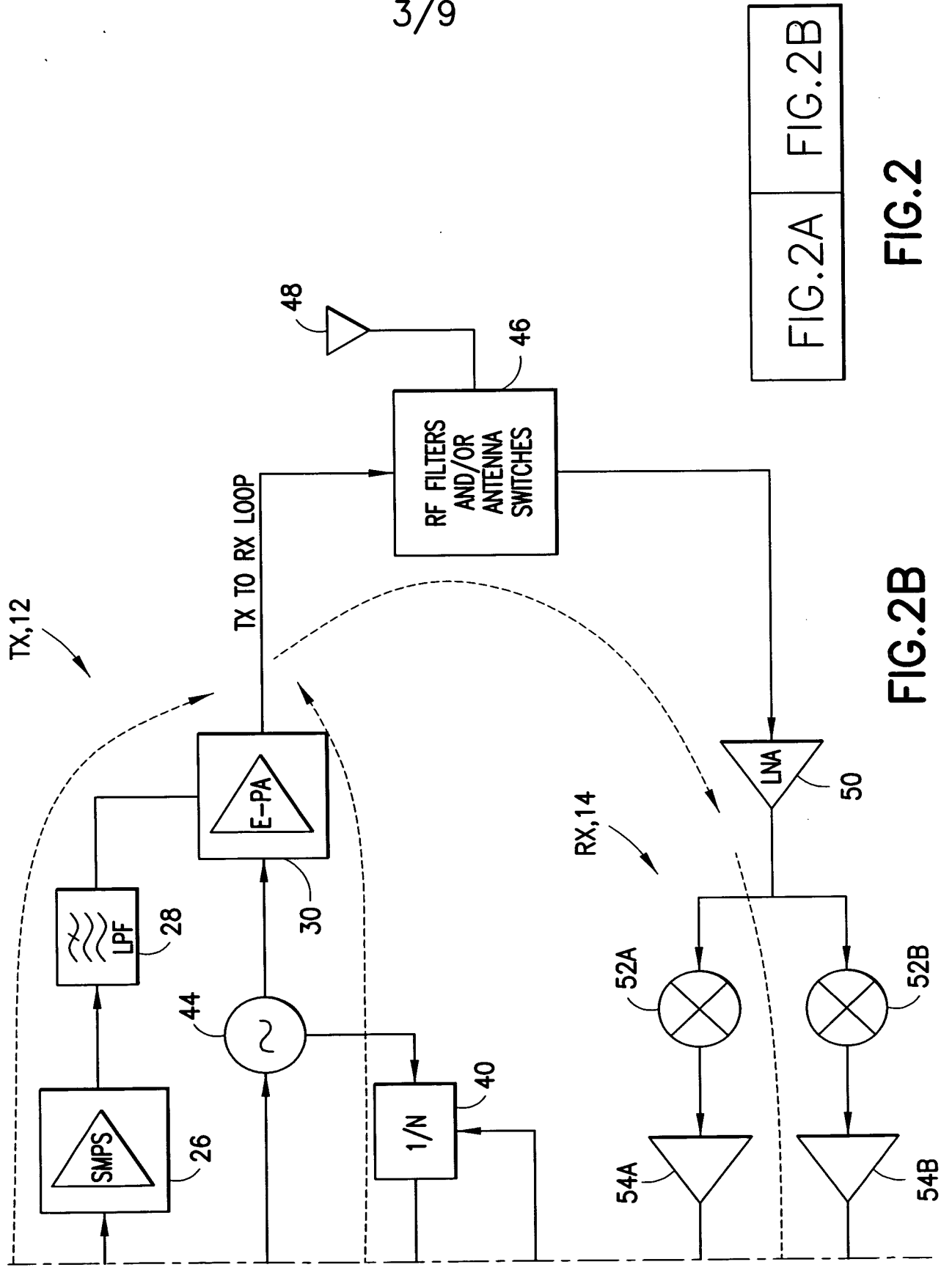


FIG. 2A FIG. 2B

FIG. 2

FIG. 2B

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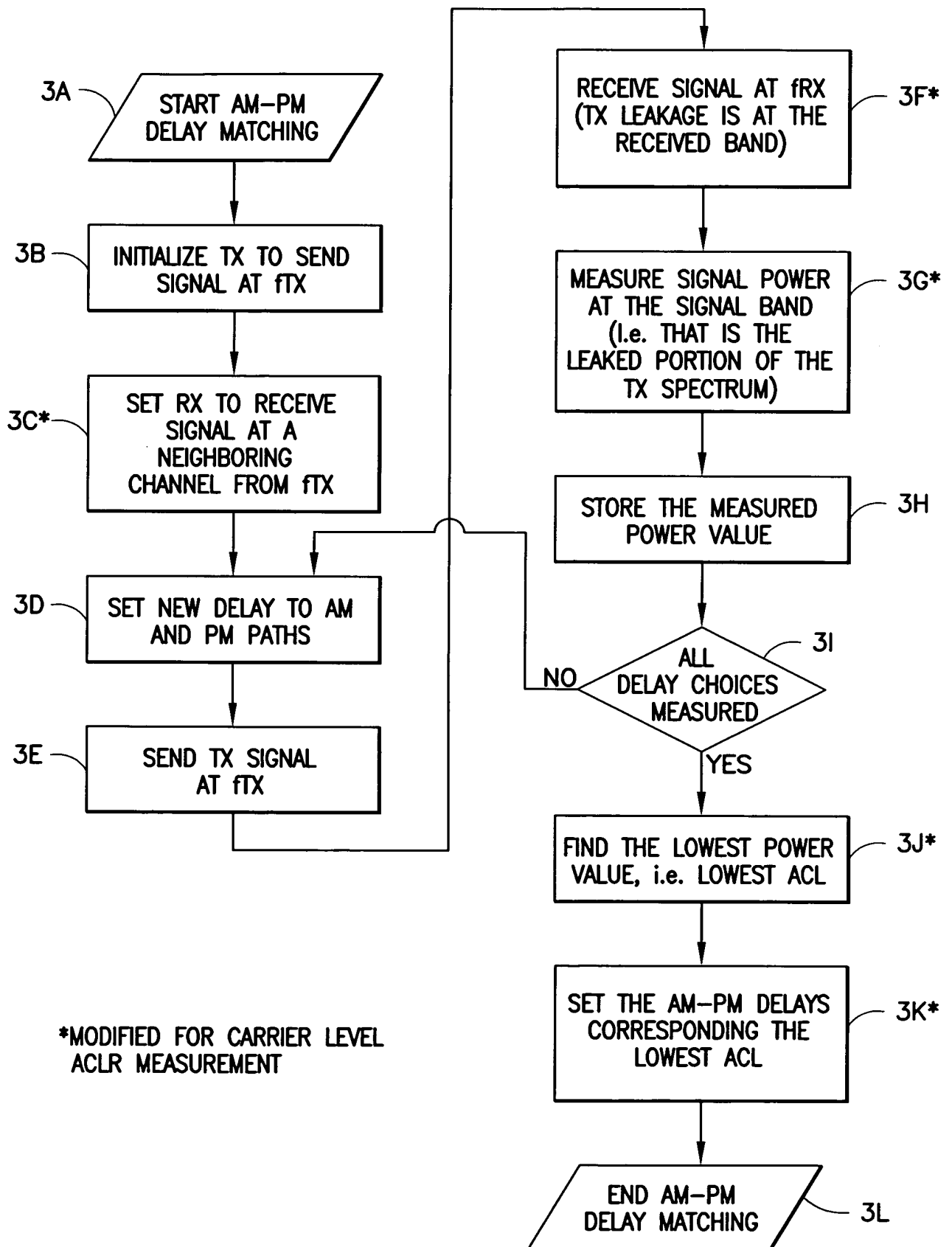


FIG.3

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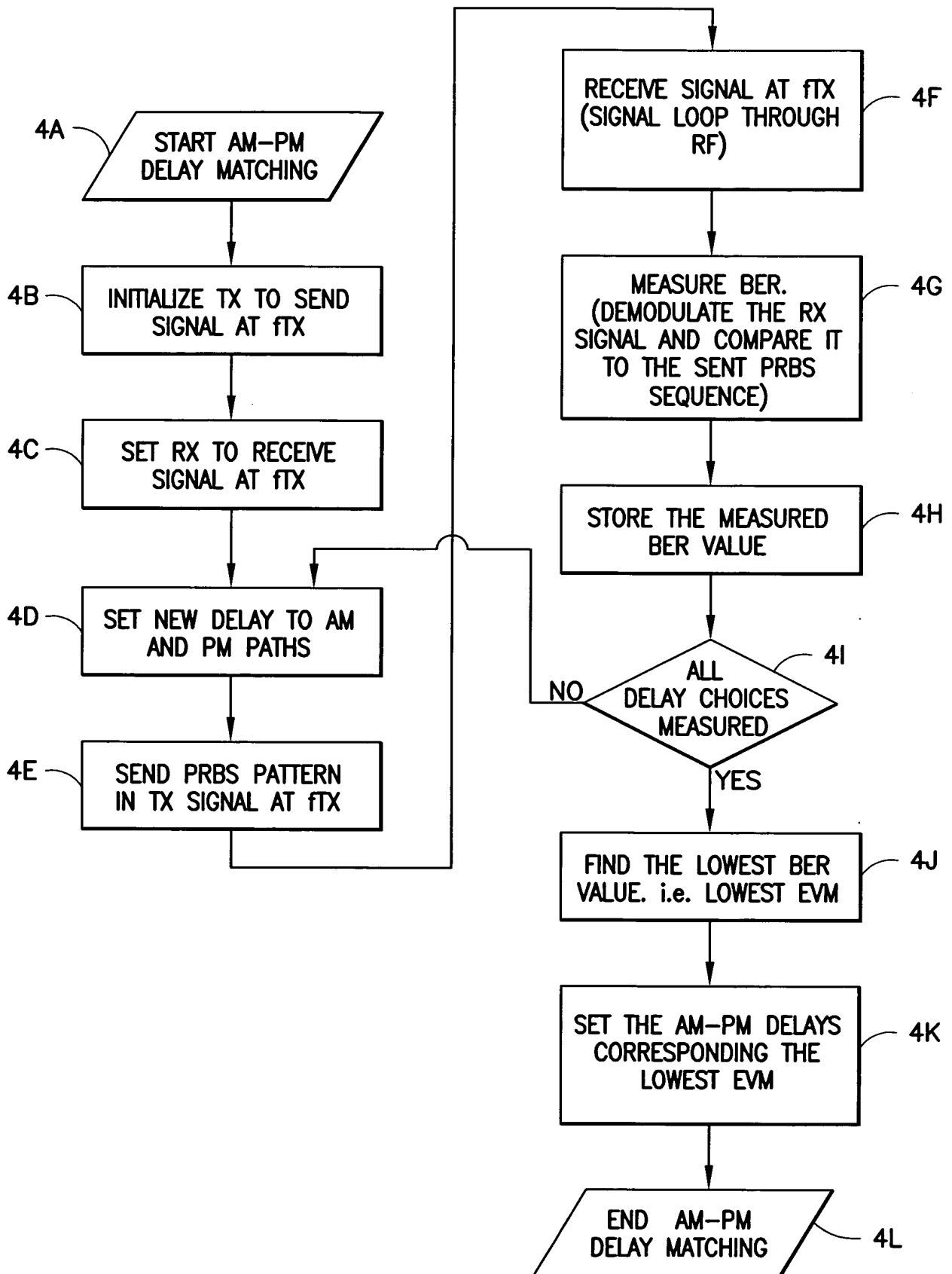


FIG.4

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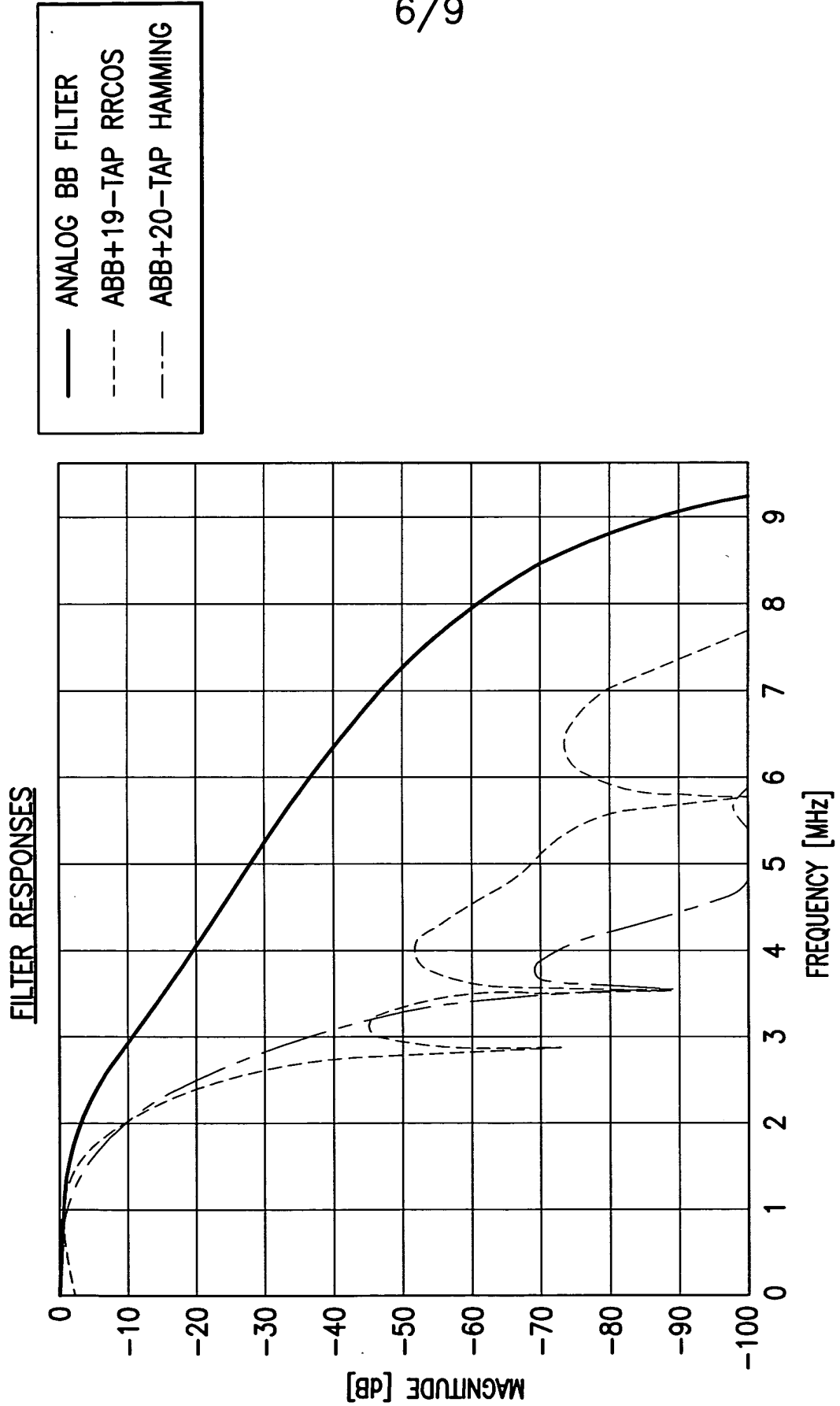


FIG.5

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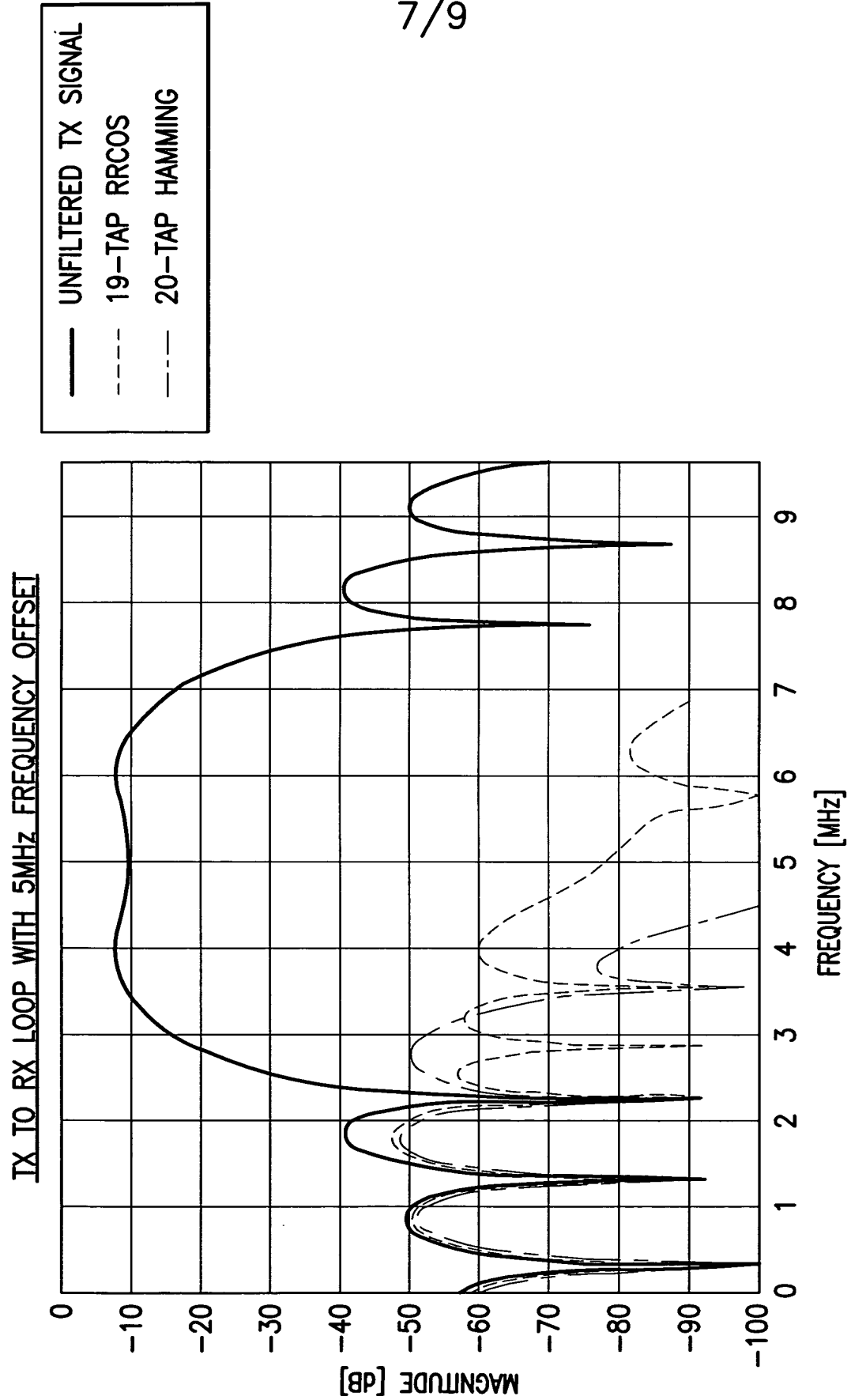
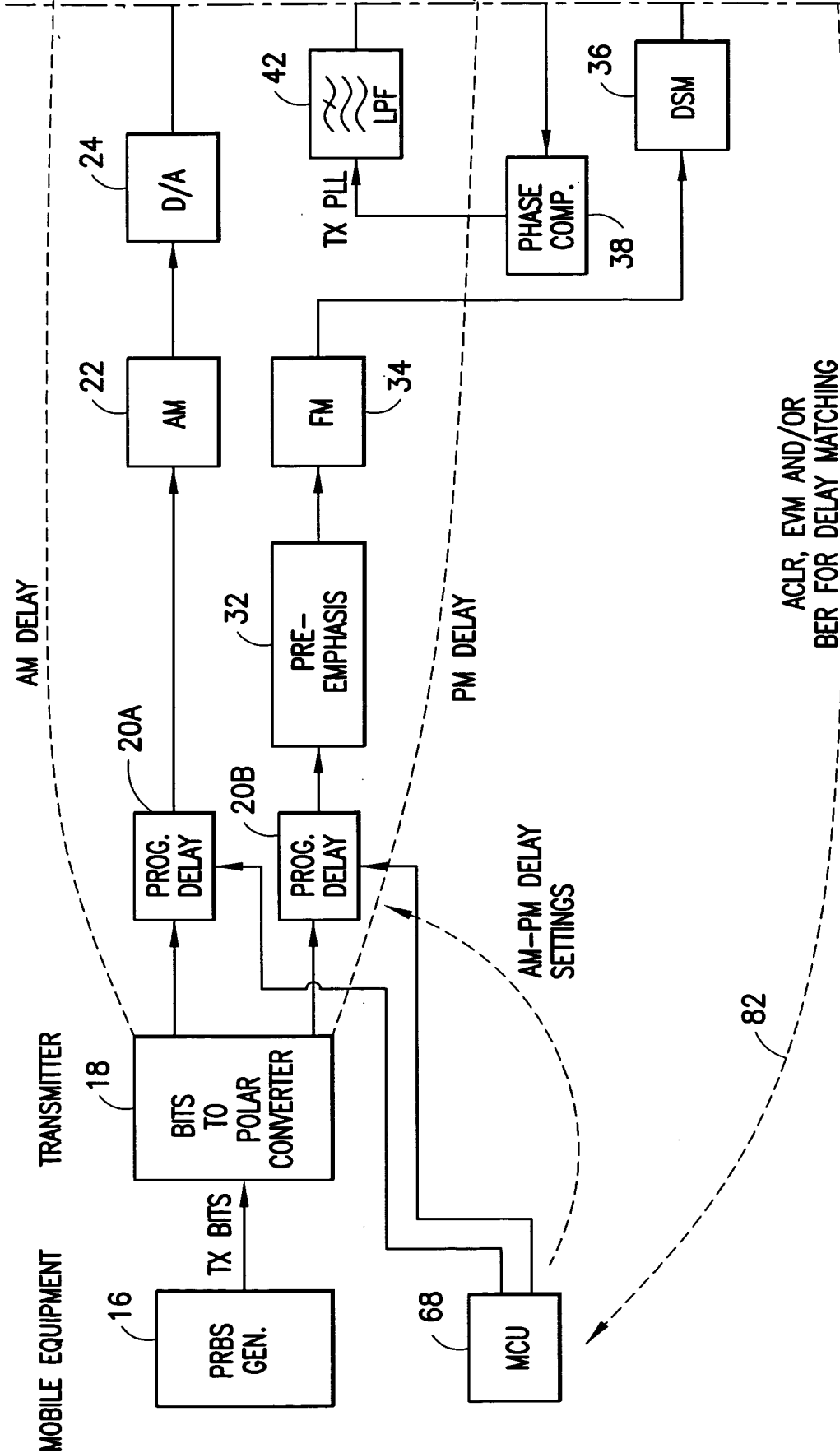


FIG.6

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ACLR, EVM AND/OR
 BER FOR DELAY MATCHING

FIG. 7A

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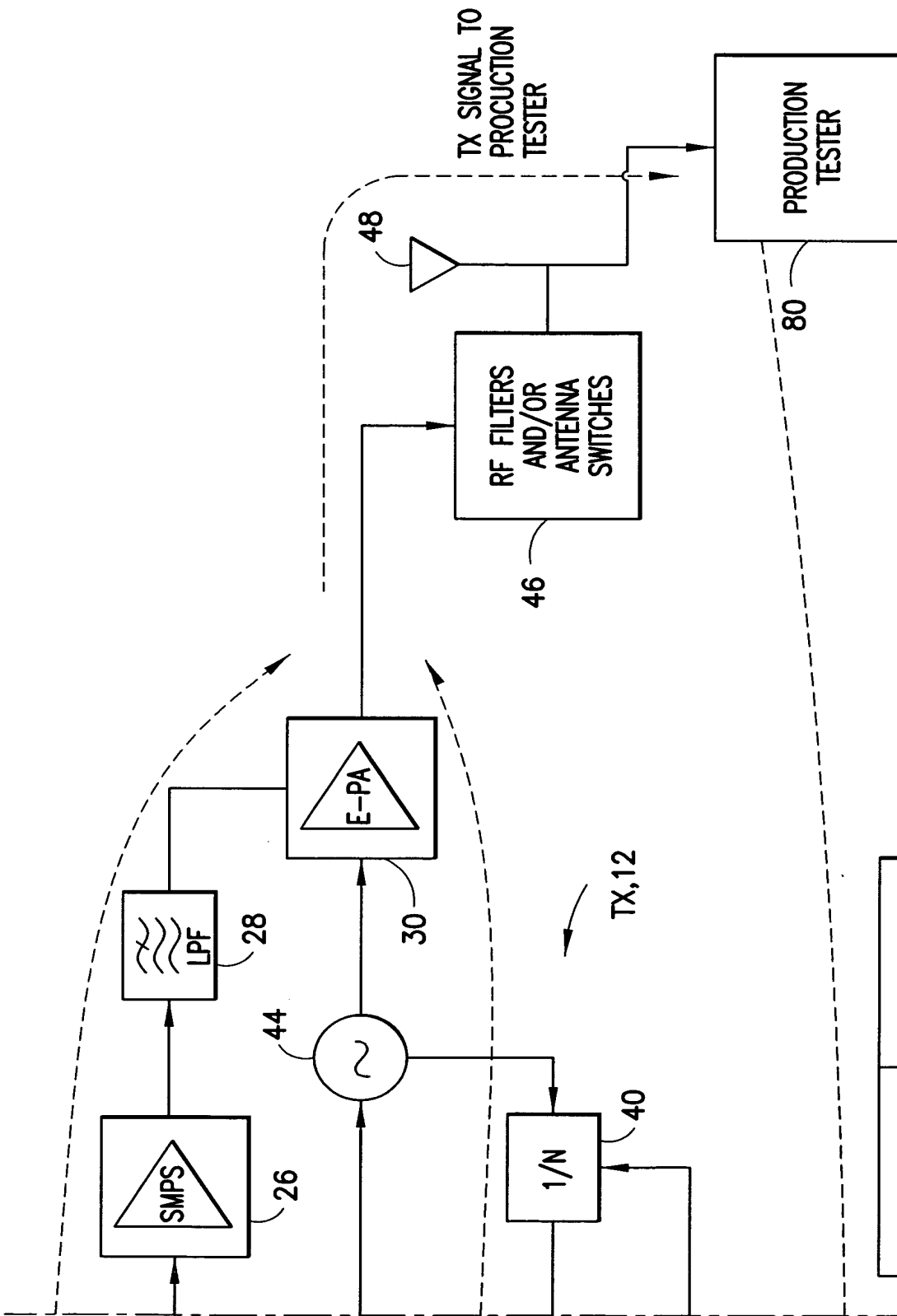


FIG. 7A FIG. 7B

FIG. 7B

FIG. 7